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Dong-Woo Jee received the B.S., M.S., and Ph.D. degrees in electronic and electrical engineering from Pohang University of Science and Technology (POSTECH), Korea, in 2006, 2009, and 2013, respectively. From 2011 to 2012, he was a Visiting Researcher with the University of Michigan, Ann Arbor, MI, USA. From 2013 to 2015, he worked as an Analog IC Designer at Biomedical Circuit group of IMEC in Leuven, Belgium. From 2021 to 2022, he was a Visiting Scholar with Arizona State University, Tempe, AZ, USA. In 2015, he joined Ajou University, where he is currently a professor. His research interests include circuit techniques for analog/digital frequency synthesizer phase-locked loop, ultra-low-power clock generation for sensor node system, sensor interface circuits, in-sensor computing, and mixed signal circuits for biomedical applications. Dr. Jee was the recipient of the Gold Prize at the 17th HumanTech Paper Award hosted by Samsung Electronics in 2011.